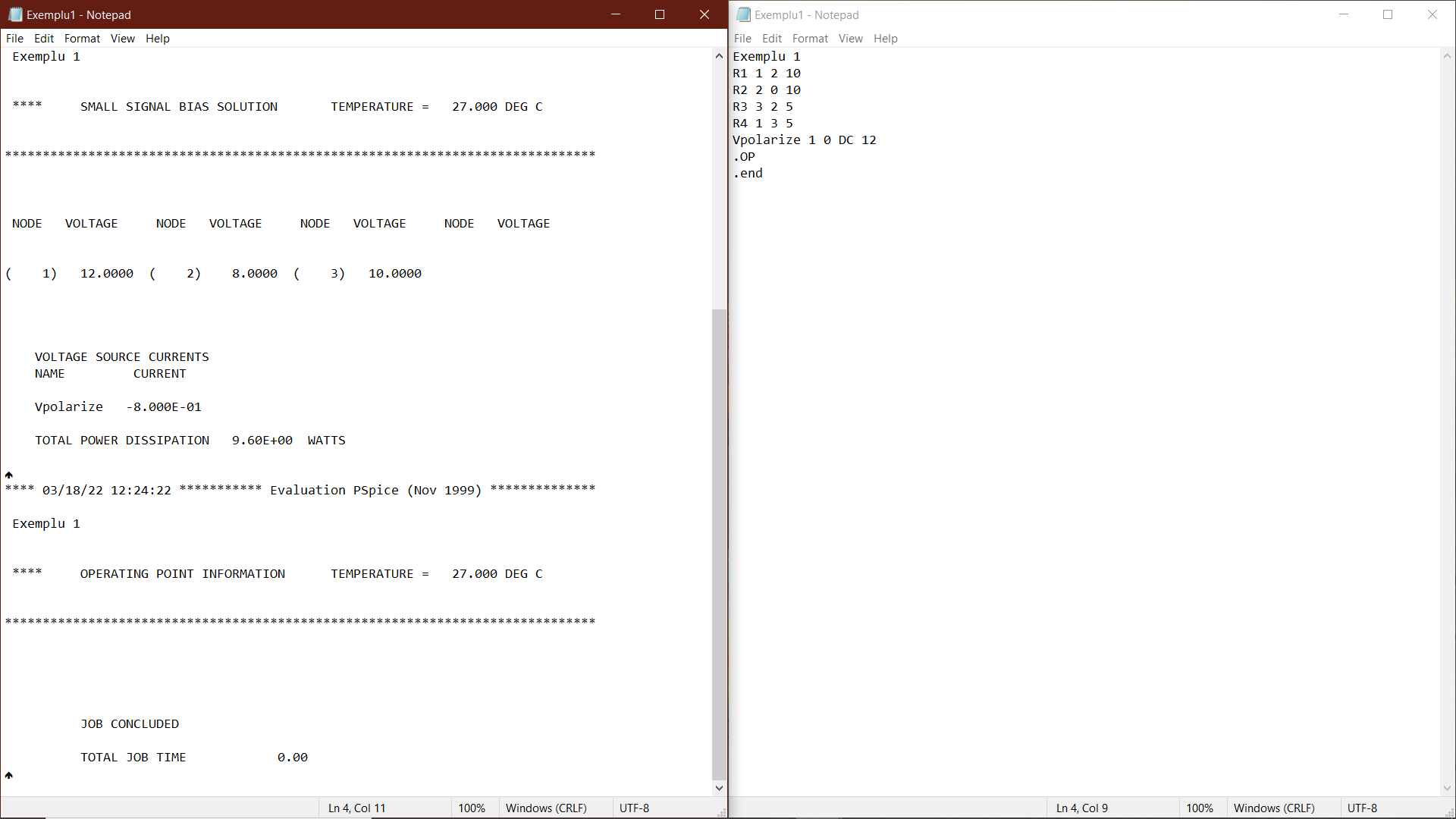
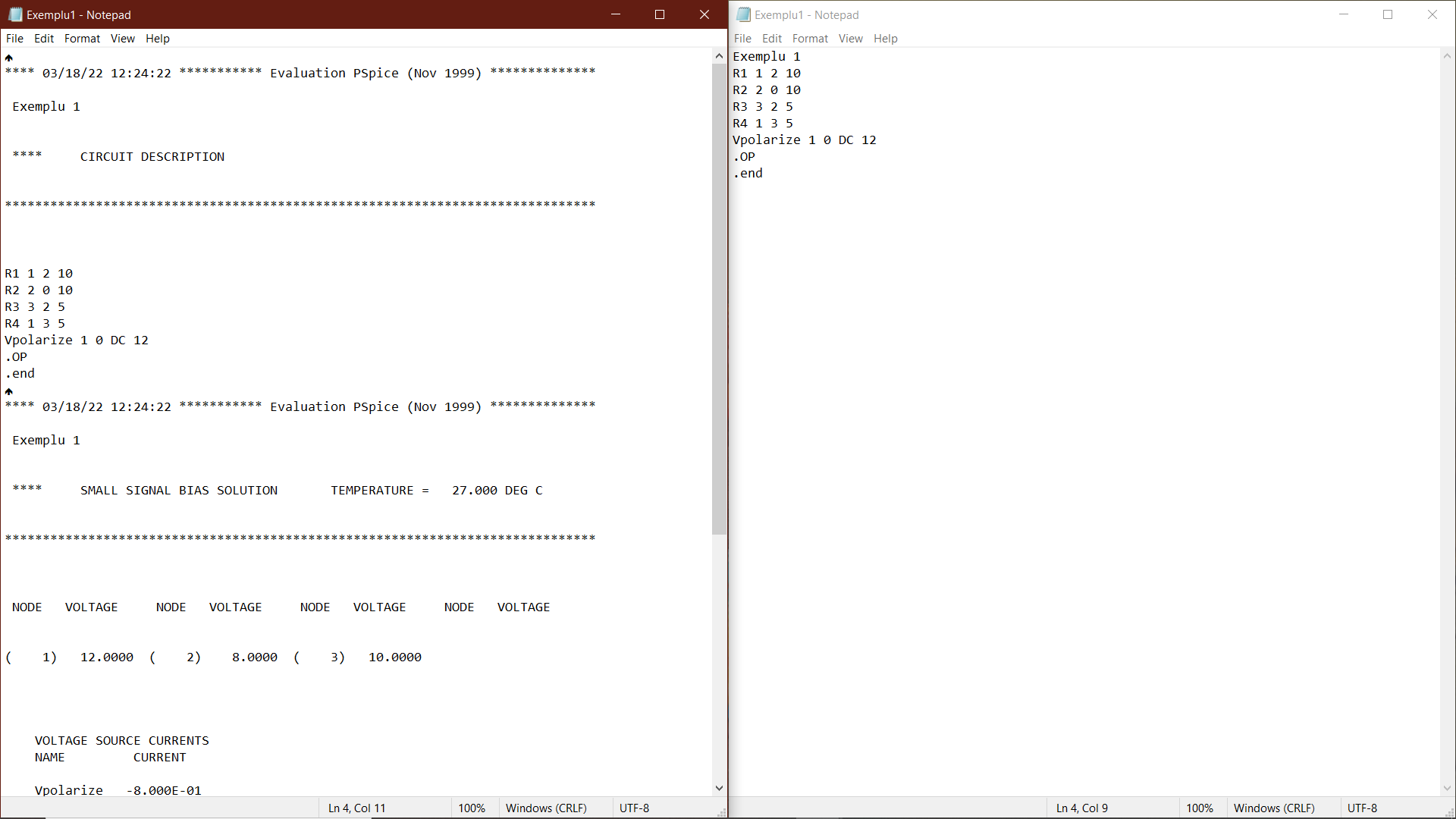
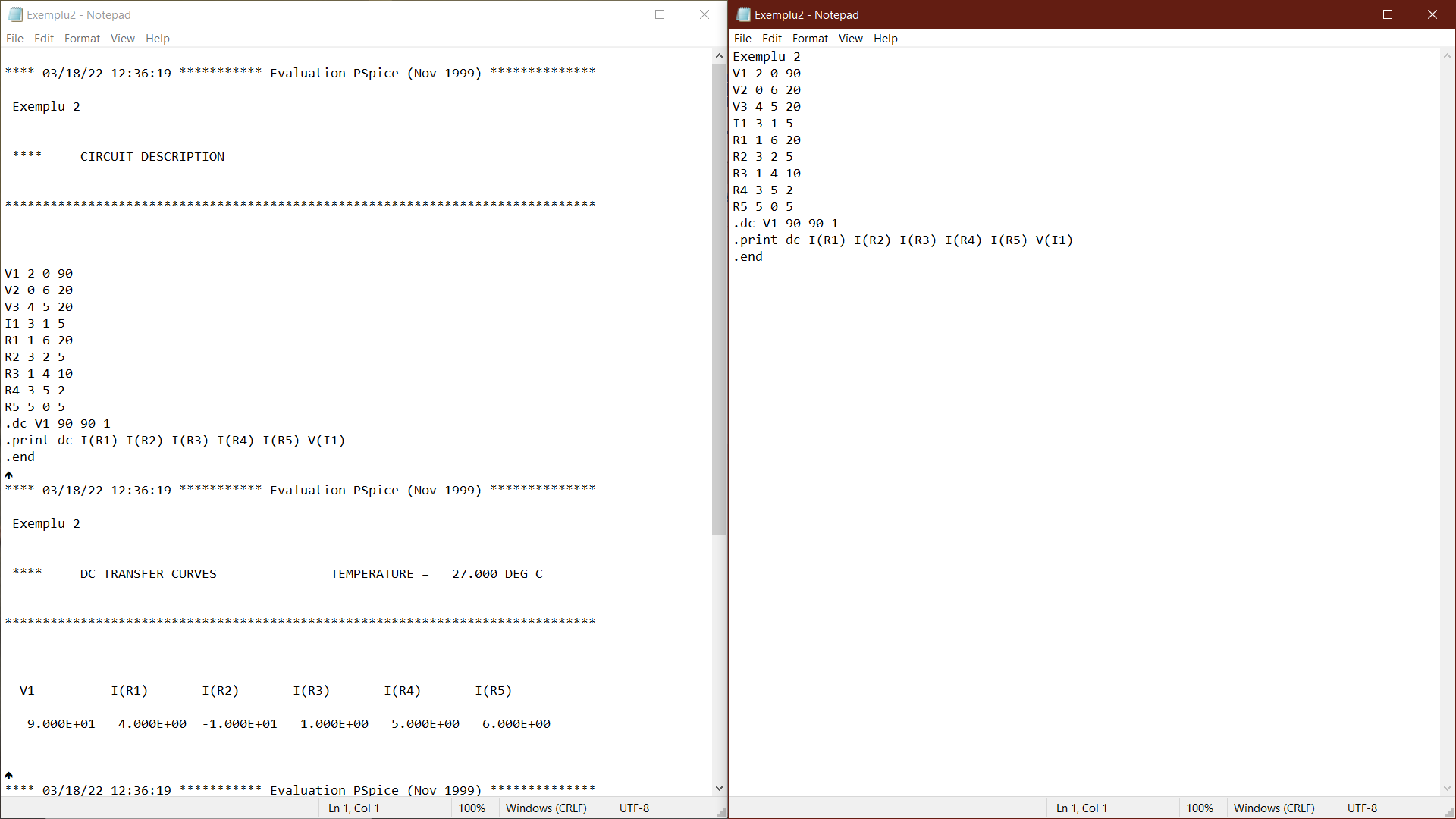
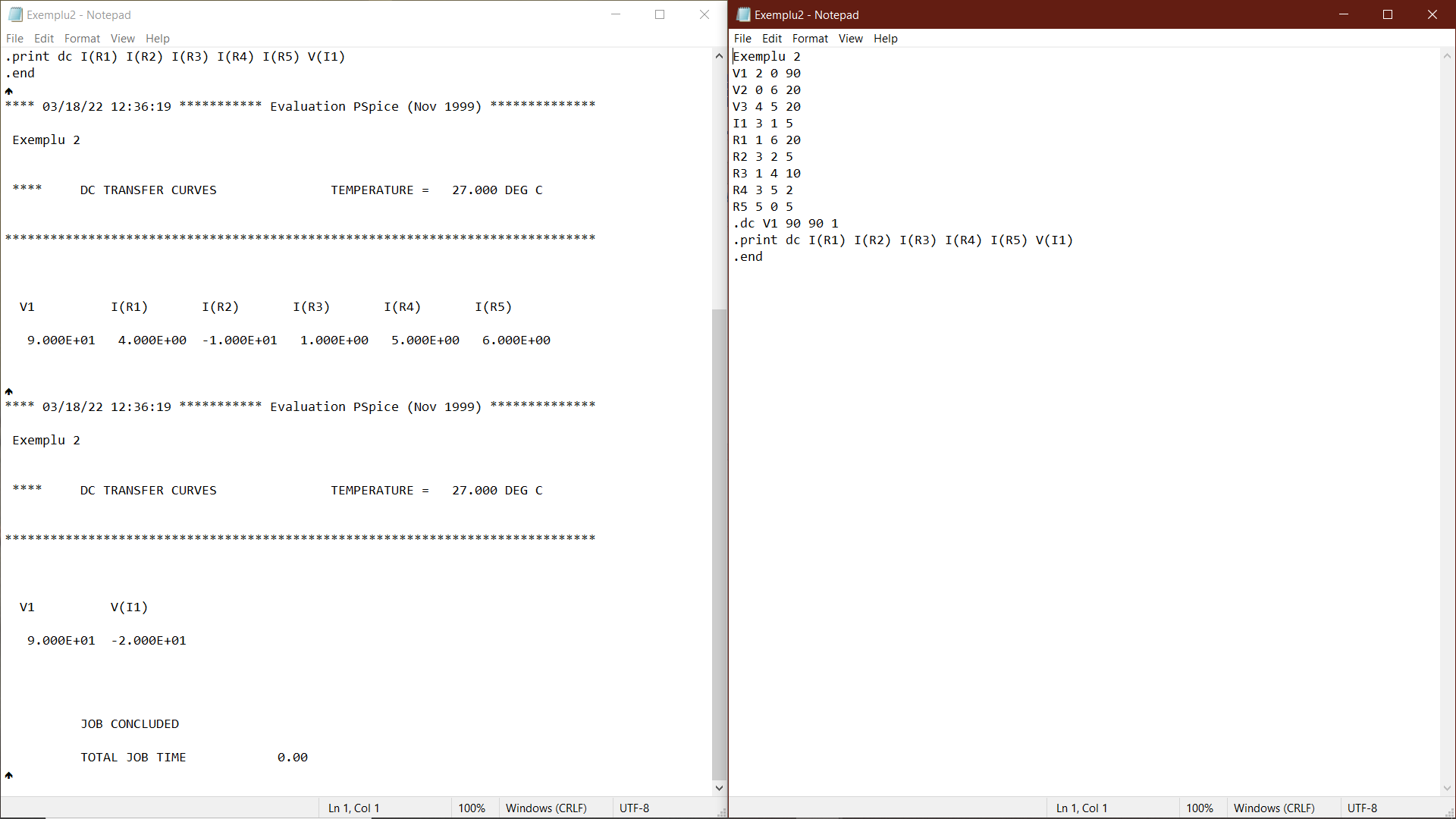
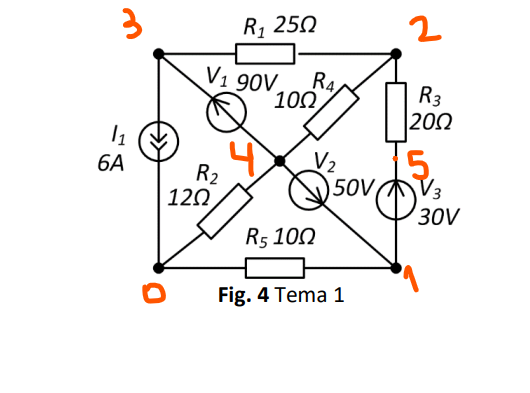
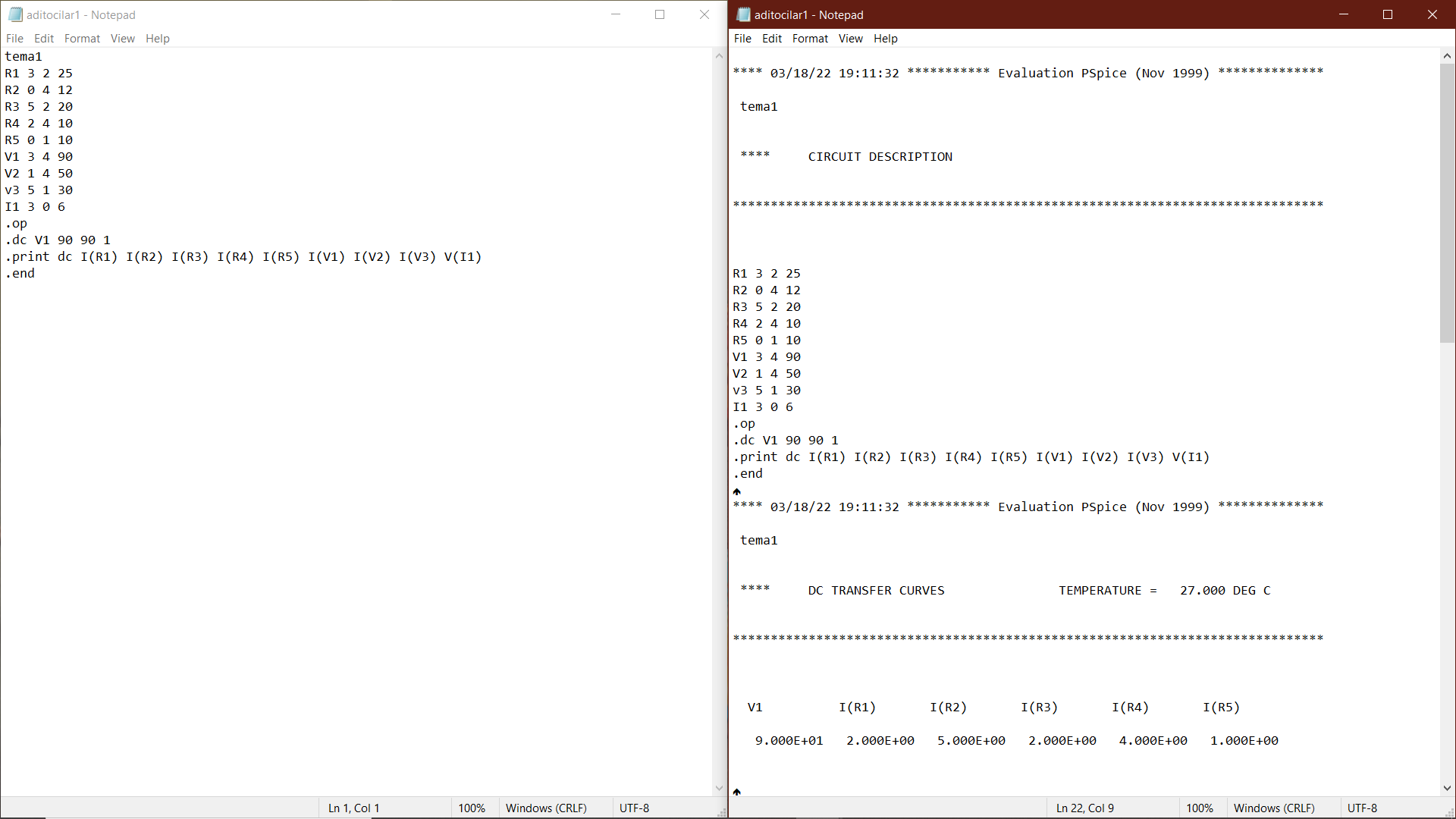
**Laborator 2 – Electrotehnica**

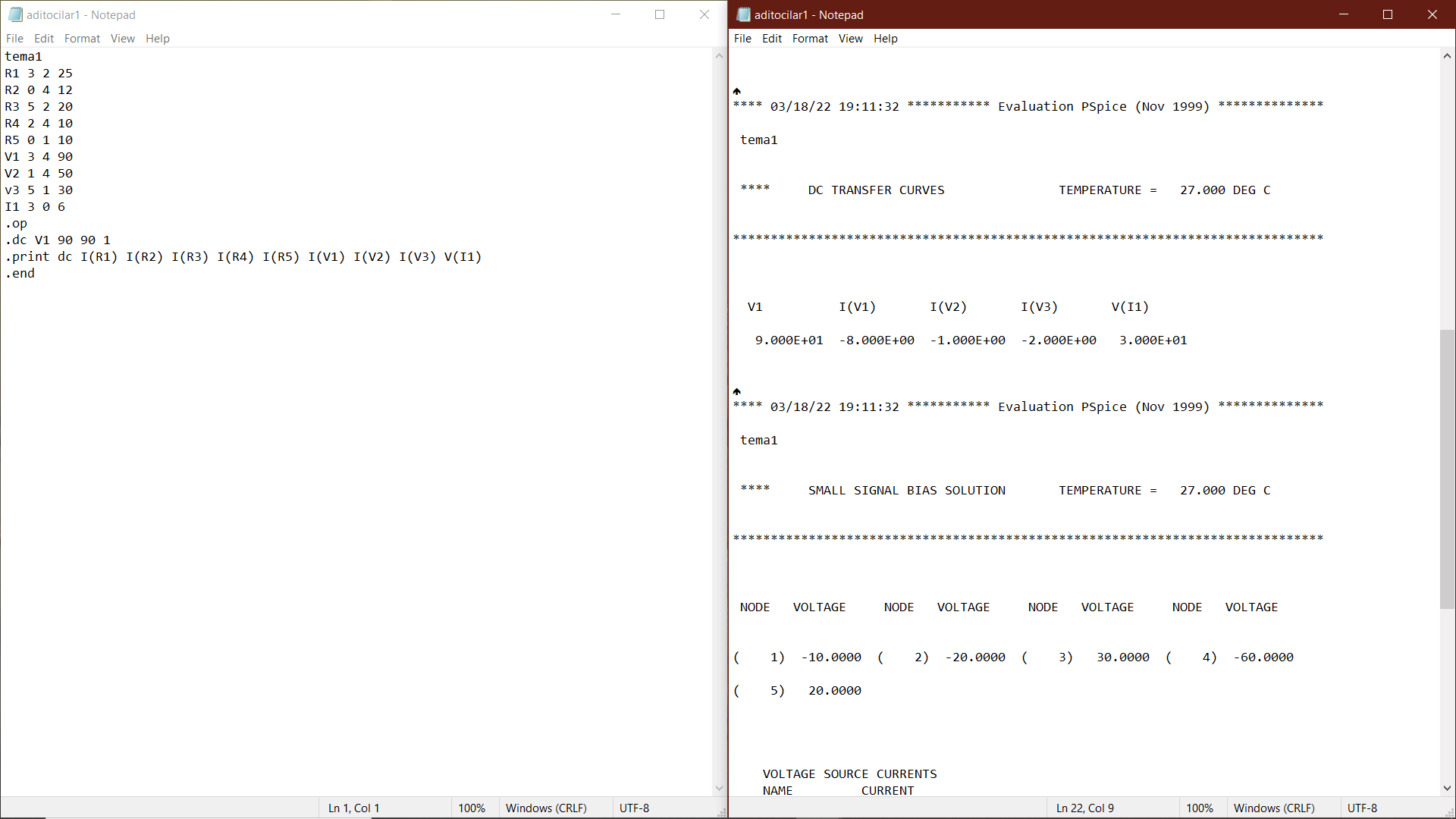
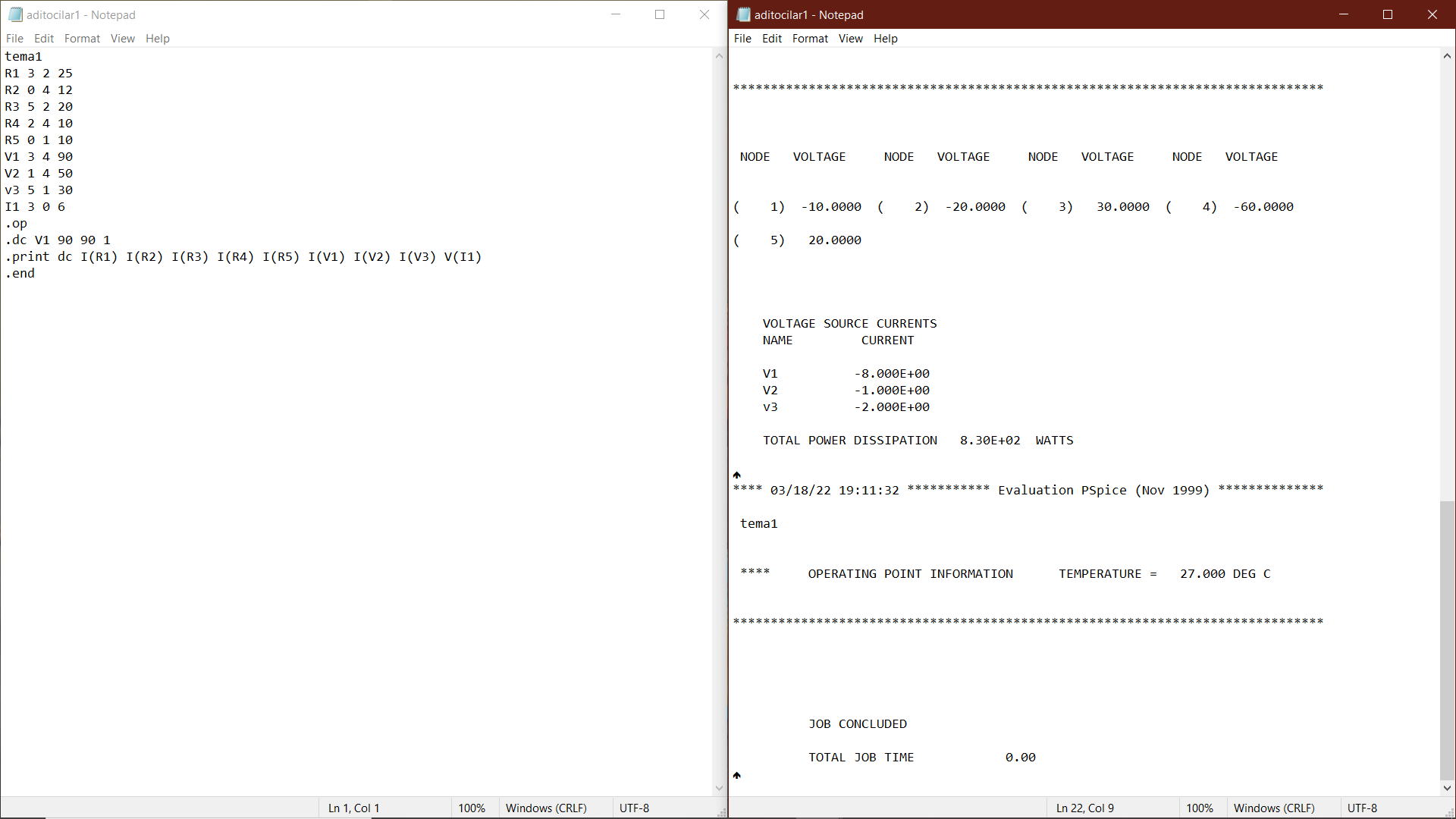
**Ex 1**

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**Ex 2**

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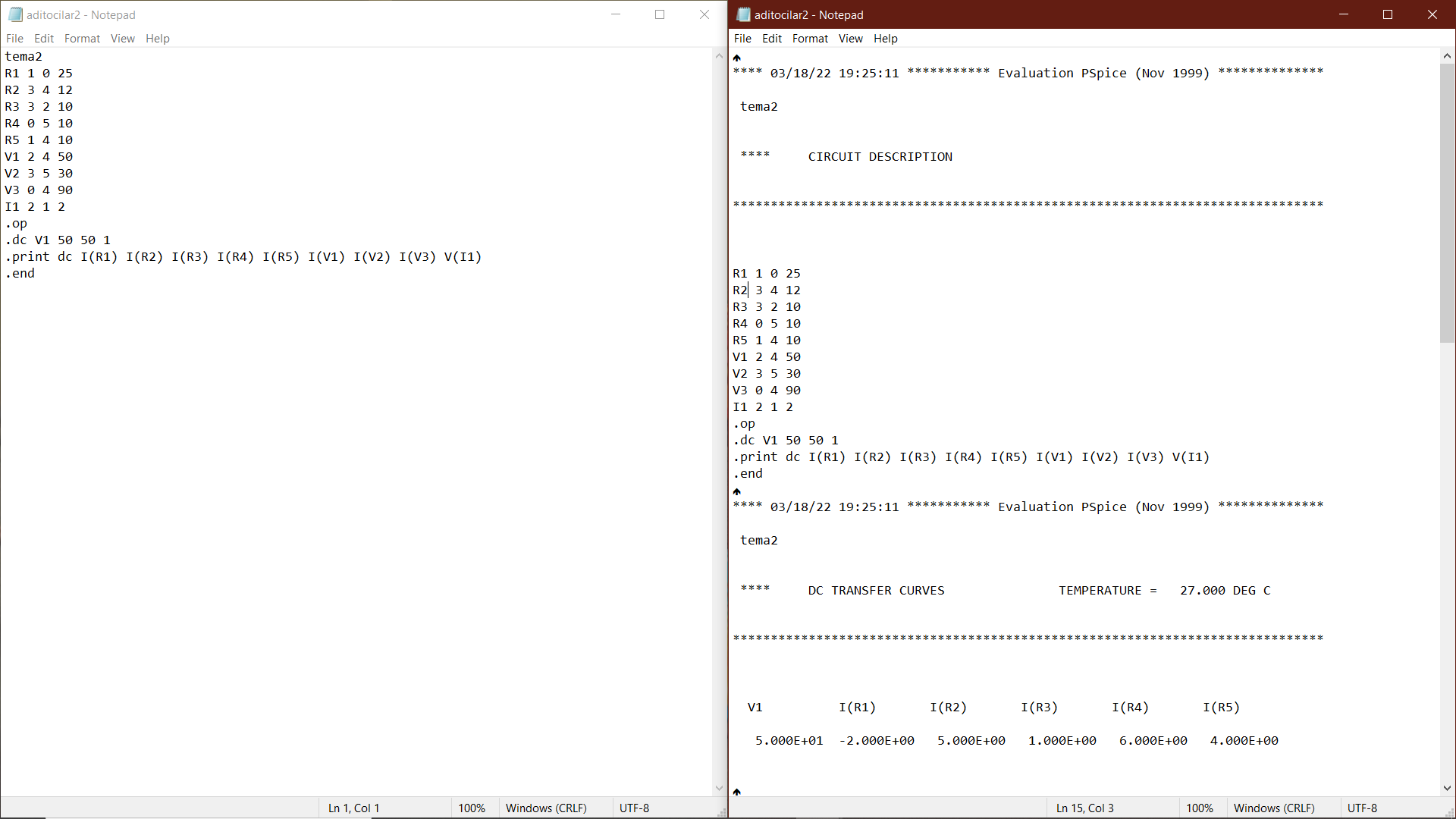
**Tema 1**

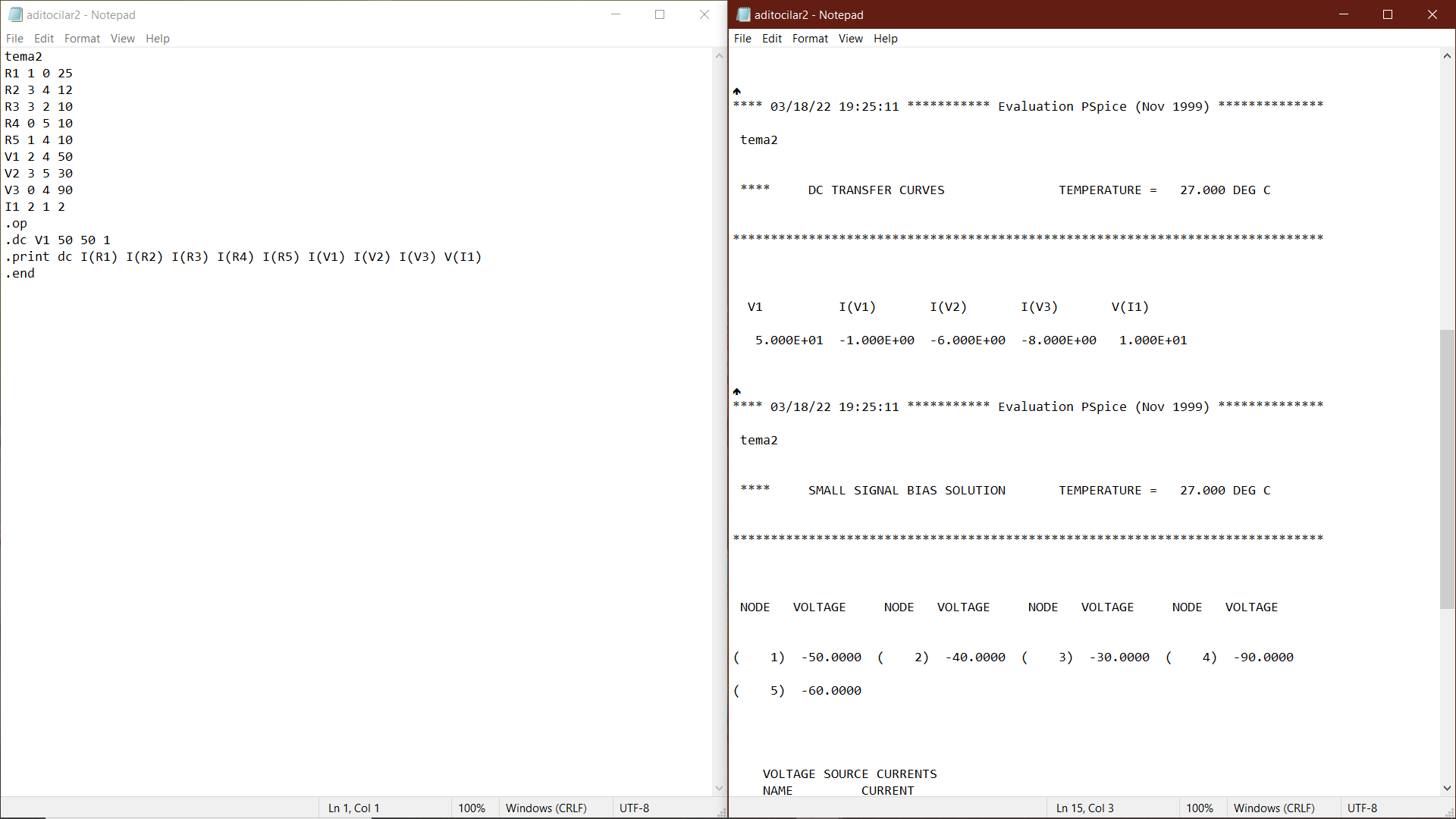
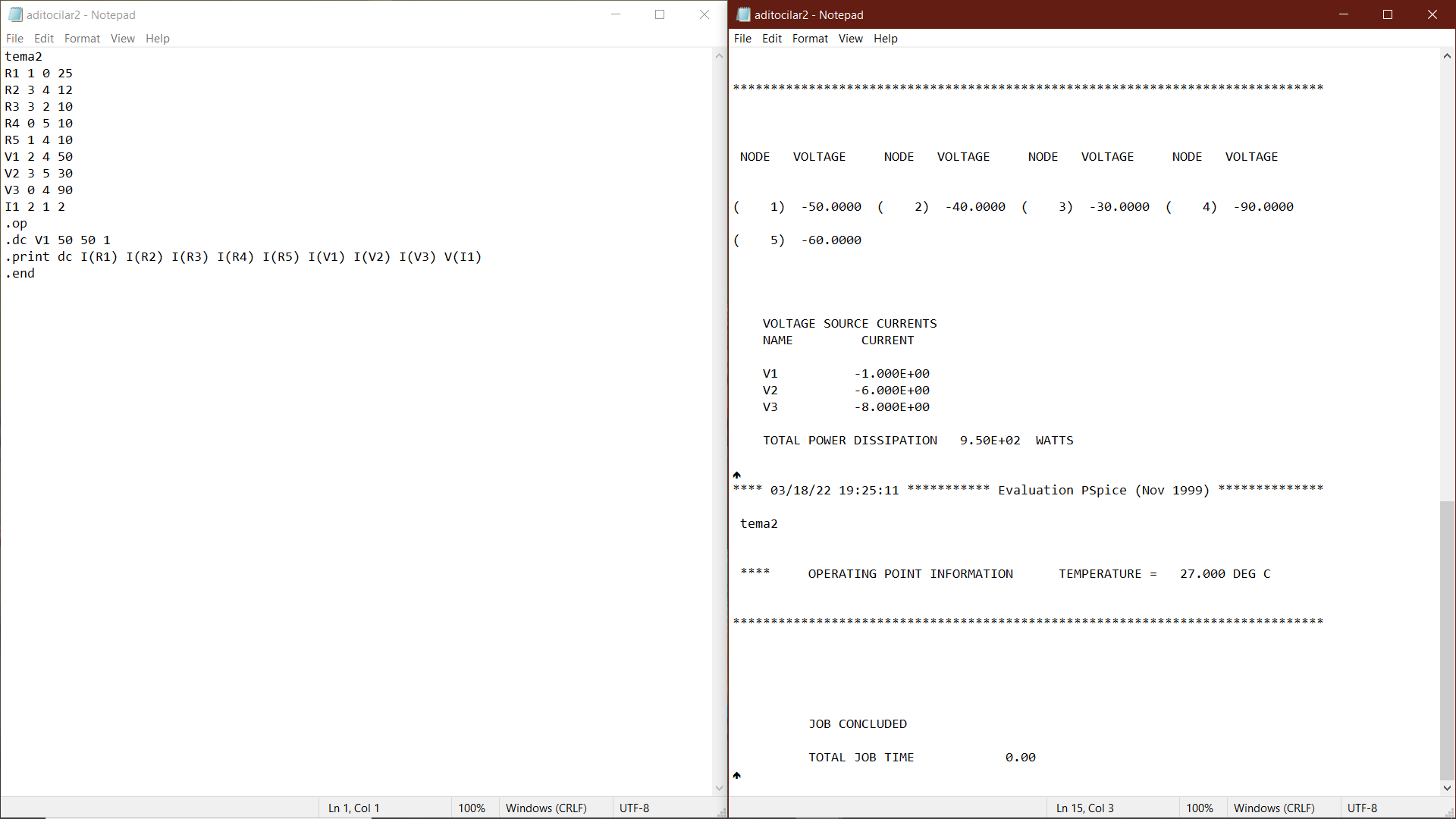
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**Tema 2**

**Diagram, schematic

Description automatically generated**

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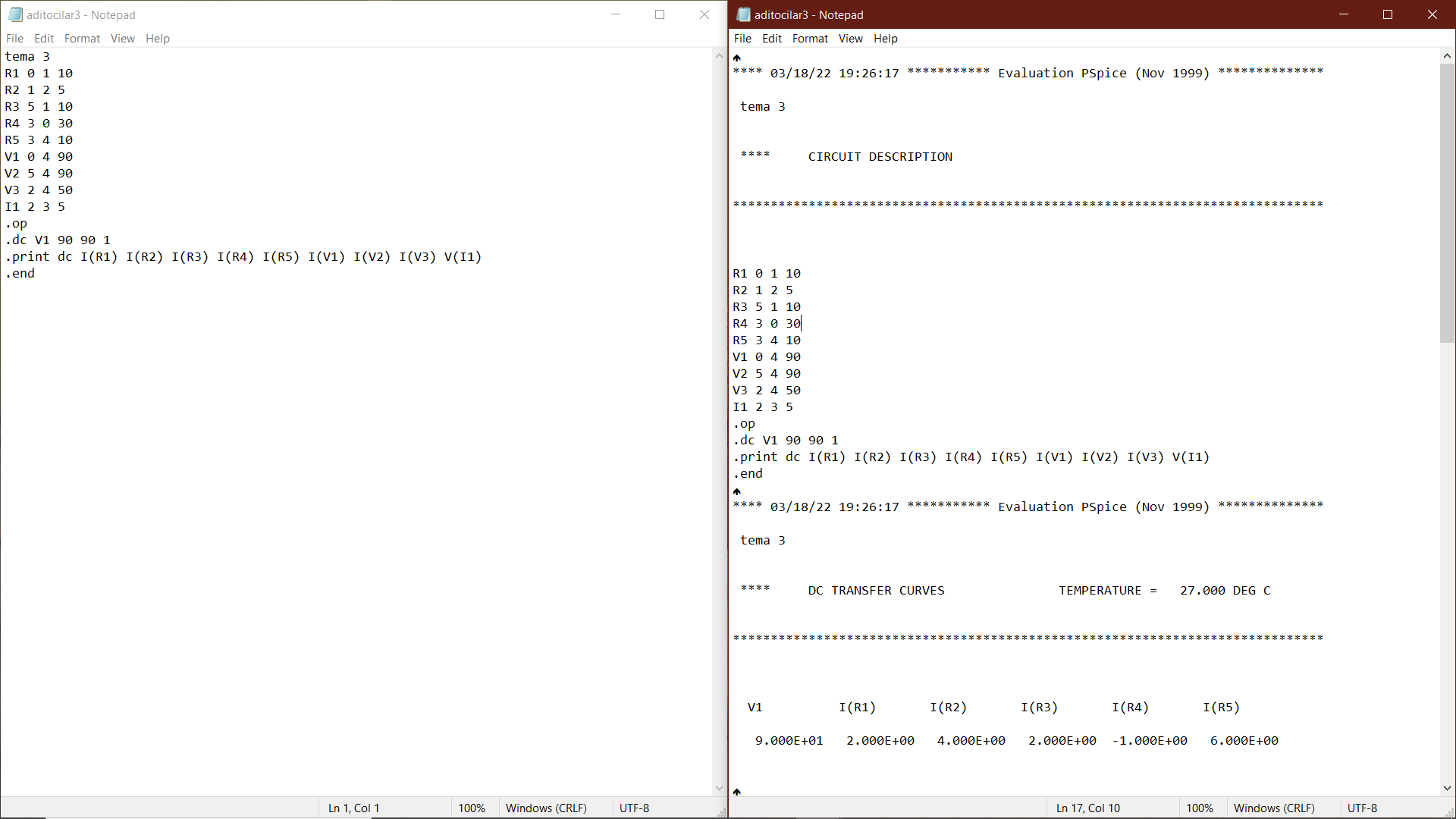
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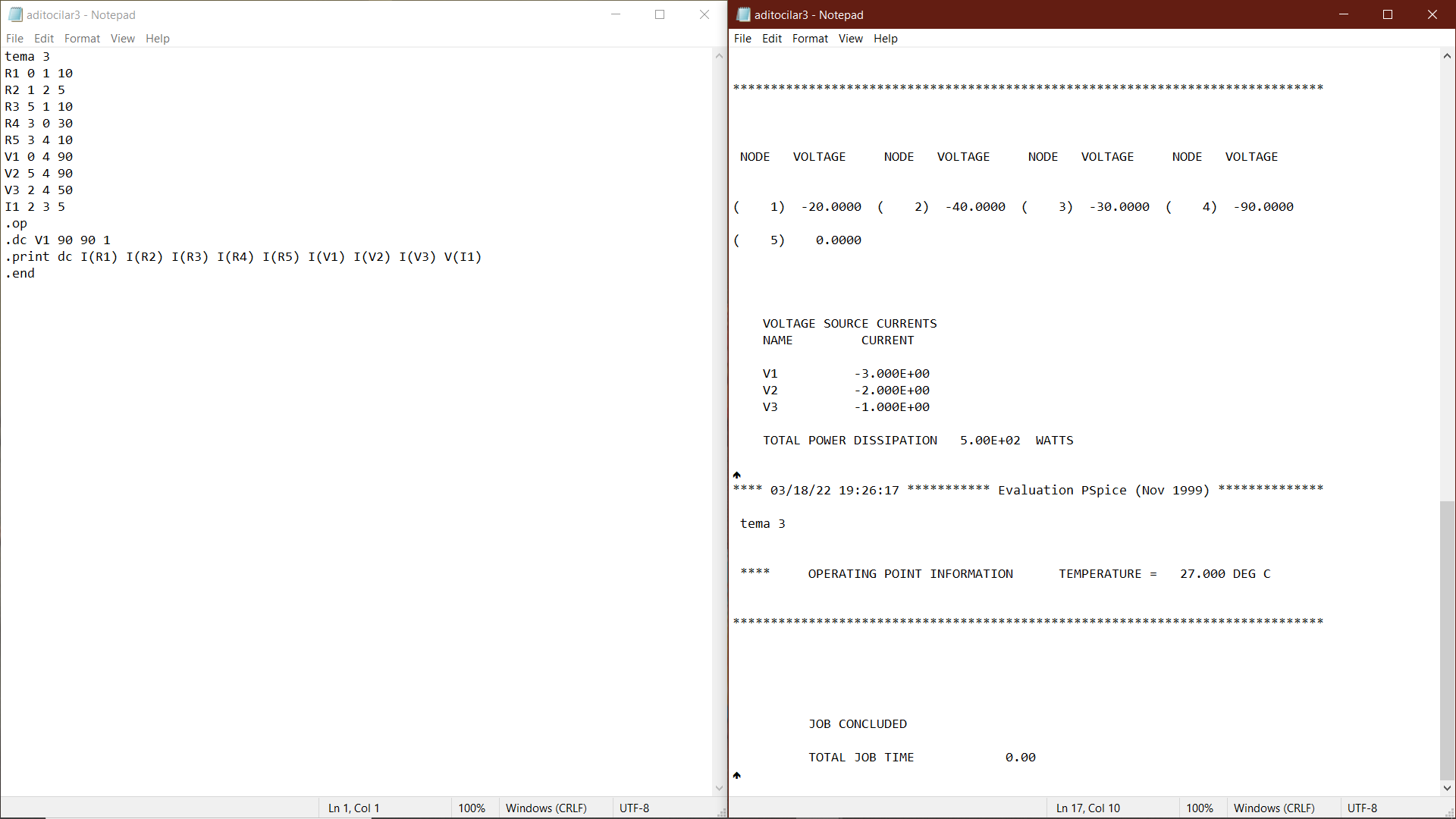
**Diagram, schematic

Description automatically generatedTema 3**

**Graphical user interface, application

Description automatically generated**

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